

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: William J. Price  
Assignee: 3PARdata, Inc.  
Title: Isolation of I2C Buses in a Multiple Power Domain Environment Using Switches  
Serial No.: 09/755,254 Filing Date: January 4, 2001  
Examiner: Eric Chang Group Art Unit: 2116  
Docket No.: M-8504 US  
(3PD-M-8504 US)

San Jose, California  
November 6, 2006

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 23313-1450

**SUBMISSION OF REPLACEMENT FORMAL DRAWINGS**

Dear Sir:

Applicant submits twelve (12) sheets of formal drawings. Please replace the previously filed drawings with the enclosed revised Figs. 1, 2A, 2B, 3A, 3B, 3C, 3D, 4A, 4B, 5, 6, 7A, 7B, 7C, 8, 9, and 10 in the above-identified patent application, as per Notice of Allowability.

If there are any questions regarding these drawing, please call the undersigned at (408) 382-0480.

Certificate of Electronic Transmission

I hereby certify that this correspondence is being submitted electronically to the United States Patent and Trademark Office using EFS-Web on the date shown below.

/David C Hsia/	November 6, 2006
Attorney for Applicant(s)	Date of Signature

Respectfully submitted,

/David C Hsia/

David C Hsia  
Attorney for Applicant(s)  
Reg. No. 46,235

PATENT LAW  
GROUP LLP  
2035 N. FIRST ST.  
SUITE 223  
SAN JOSE, CA 95134  
(408) 382-0480  
FAX (408) 382-0481